

Form PTO-149 FEB 11 2003 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)		ATTY. DOCKET NO. M122-1670		SERIAL NO. 09/881,407	
				APPLICANT Zhongze Wang		FILING DATE June 13, 2001	
				GROUP 2822			
U.S. PATENT DOCUMENTS							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation
							Yes No
	AM						
	AN						
	AO						
	AP						
	AQ						
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
PEP	AR		Wolf, Stanley, Ph.D., "Silicon Processing for the VLSI Era", Vol. 2: Process Integration, Lattice Press 1990, pgs. 348-363				
	AS						
	AT						
EXAMINER <i>Paul P. P.</i>				DATE CONSIDERED <i>June 23, 2003</i>			
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

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